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PATENT

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Serial No.:	09/766,014	Group Art Unit:	2816
Filed:	January 18, 2001	Docket No.:	2316.1547US01
Title:	APPARATUS AND METHOD FOR EXTRACTING DATA VALUES FROM A SIGNAL ENCODED WITH AES3 DATA		

MARKED-UP VERSION SHOWING CHANGES MADE TO SPECIFICATION

First paragraph of Page 3:

The count value that is supplied by the counter 14 is provided to a discriminator 18, a downcounter 22, a comparator 26 and a register 30. The output of the comparator 26 is connected to the load input of the downcounter 22 and to the write enable input of the register 30. If the output signal of the comparator 26 is high, the count value supplied by the counter 14 is written into the register 30 and is also loaded into the downcounter, which then counts down at a fairly low rate. The rate at which the downcounter counts down is selected [to]so that the downcounter will not count to 2/3 or less of its initial value in the subframe interval corresponding to the lowest sample rate. The output of the downcounter is supplied to the comparator 26.

First paragraph of Page 4:

The value loaded into the register 30 is supplied to the address input of a lookup table 34 having a 16 bit data output. The lower byte (bits 0-7) of the word returned by the lookup table in response to the count value of the wide pulse corresponds to the duration of the half pulse whereas the upper byte (bits 15-8) corresponds to the duration of the whole pulse. In the case of the count value being 107, the lower byte is decimal [46]36 and the upper byte is decimal 71.

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